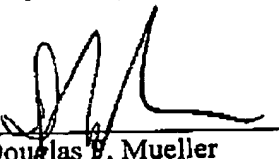App. No. 09/897,574
Office Action Dated March 21, 2006

Claim 1. A data transfer apparatus comprising:

an associative memory; and

a controller connected between a system bus and a local bus for controlling data input/output of the associative memory;

wherein the controller of the data transfer apparatus, the data transfer apparatus being a first device, fetches an address and data that are transferred between a second device and a third device that are connected only on the system bus so as to duplicate and store them in the associative memory, and

when a fourth device on the local bus generates a read cycle to read data from a read address associated with one of the second and third devices on the system bus and the read address is contained in the address stored in the associative memory, the controller reads out corresponding data from the associative memory so as to transfer it to the local bus.

Applicant courteously requests that the Examiner's Amendment be corrected in accordance with the above, and that the undersigned be notified of the correction. Any questions regarding this communication can be directed to the undersigned attorney, Douglas P. Mueller, Reg. No. 30,300, at (612)455-3804.

Respectfully Submitted,

Dated: August 1, 2006

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